



**राष्ट्रीय प्रौद्योगिकी संस्थान आंध्र प्रदेश**  
**NATIONAL INSTITUTE OF TECHNOLOGY ANDHRA PRADESH**  
(An autonomous Institute under the aegis of Ministry of Education, Govt. of India)  
Tadepalligudem – 534101 West Godavari District, Andhra Pradesh, India  
**Department of Electronics and Communication Engineering**

Date:23/10/2024

**JRF recruitment Notification**

Applications are invited from interested candidates for two posts of Junior Research Fellow (JRF) to work for the project “Energy Efficient Mesh Architecture Based Indigenous Neuromorphic Processor for Extreme Edge IoT Applications” sanctioned by MEITY under Chips-to-startup (C2S) scheme with Dr. Kiran Kumar Gurrala as PI and Dr. Puli Kishore Kumar as C-PI, Department of Electronics and Communication Engineering, NIT Andhra Pradesh, Tadepalligudem.

Details of Qualifications, Emoluments, and Duration are given below:

S. No.	Particulars	Details
1.	Name of the Position	<b>Junior Research Fellow (JRF)</b>
2.	Essential Qualifications	<p>1. Candidates with a Master’s degree in allied specializations of Electronics and Communication Engineering. Candidates belong to UR/EWS/OBC-NCL category must have secured 60% (or above) aggregate marks (CGPA of 6.5) either at UG or PG level examination. For SC/ST/PWD candidates, 55% aggregate (or above) marks or equivalent CGPA of 6.0 is essential either at UG or PG level.</p> <p style="text-align: center;">OR</p> <p>Candidates with Bachelor’s in Electronics and Communication Engineering or allied branches from any Centrally funded Technical Institutes (CFTI). Candidates belong to UR/EWS/OBC-NCL category must have secured 75% (or above) aggregate marks or equivalent CGPA of 8.0, and for the SC/ST/PWD candidates, 70% aggregate (or above) marks or equivalent CGPA of 7.5.</p> <p>2. Candidates should qualify in National Eligibility Tests -CSIR-UGC NET including lectureship (Assistant Professorship) or GATE.</p>
3.	Desired skills& experience	Candidates with expertise in EDA tools along with domain knowledge in VLSI, Machine Learning/Deep Learning, Embedded systems will be preferred.
4.	Project Title	<b>Energy Efficient Mesh Architecture Based Indigenous Neuromorphic Processor for Extreme Edge IoT Applications.</b>
5.	Sponsoring Agency and Scheme	MEITY, Chips-to-startup (C2S)
6.	Duration of Project	5 years
7.	No. of positions	02
8.	Consolidated monthly Salary	Rs. 37,000/- per month + HRA (As applicable) during the 1 <sup>st</sup> & 2 <sup>nd</sup> Year. Rs. 42,000/- per month + HRA (As applicable) during the 3 <sup>rd</sup> Year.
9.	Investigators	Dr. Kiran Kumar Gurrala    Dr. Puli Kishore Kumar Asst. Prof., DECE-PI        Asst. Prof., DECE-Co-PI
10.	Department	Electronics and Communication Engineering
11.	Tenure of	05 years or till the completion of the project, whichever is earlier.



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	Assignment	
12.	Age Limit	35 years as of the closing date of the application (Age may berelaxed for exceptional candidates)
13.	Other Benefits	The selected candidate is eligible to register for Ph.D. degree at NIT Andhra Pradesh as per institute rules.
14.	Last date for receipt of applications	<b>20-11-2024</b>

**How to Apply:**

Eligible candidates may send the soft copy of the filled-in application form as given below format along with the detailed CV and educational, GATE qualification, and experience certificates to **[kirankumargurrala@nitandhra.ac.in](mailto:kirankumargurrala@nitandhra.ac.in)** (Dr. Kiran Kumar Gurrula, Assistant Professor, Department of Electronics and Communication Engineering, NIT Andhra Pradesh). No hard copies of the application will be accepted. Only the shortlisted candidates will be informed by email.

**Selection Procedure:**

The date of the Interview/Written test, Reporting time, and Mode of Interview will be intimidated to only the shortlisted candidates by e-mail. Selection will be based on qualification, experience, and interview performance.

**For more details of the project, please do contact:**

Dr. Kiran Kumar Gurrula  
Assistant Professor & PI of the Project.  
Department of Electronics and Communication Engineering  
National Institute of Technology Andhra Pradesh  
Email: [kirankumargurrala@nitandhra.ac.in](mailto:kirankumargurrala@nitandhra.ac.in)

Dr. Puli Kishore Kumar  
Assistant Professor & Co-PI of the Project.  
Department of Electronics and Communication Engineering  
National Institute of Technology Andhra Pradesh  
Email: [pulikishorek@nitandhra.ac.in](mailto:pulikishorek@nitandhra.ac.in)

**General Instructions to the applicants:**

1. Selected candidates shall not have a claim on any regular position and shall not be bestowed any of the privileges like Housing, Medical Facility, and other benefits available to regular employees of the Institute.
2. Applicants are advised to ensure, before appearing for the Selection Process, that they possess the minimum essential qualification and experience laid down for the post. Qualification obtained has to be from a recognized University/Institute. Eligibility in terms of age, qualification, and experience of a candidate shall be considered as on the last date for receipt of the application.
3. No TA/DA will be paid for attending the interview.
4. In case of any inadvertent mistake in the process of selection, which may be detected at any stage even after the issue of the appointment letter, the Institute reserves the right to modify, withdraw, or cancel any communication made to the applicants.
5. After joining the service of the Institute, the person will have to abide by the Rules, Regulations, Ordinances, Statutes, and acts of the Institute applicable from time to time. He/she may be assigned any duty within or outside the Institute depending upon the exigency of the work.



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6. In case of any dispute/ambiguity that may occur in the process of selection, the decision of the Institute shall be final.
7. For a query related to selection process, the applicant may send an email to [kirankuamrgurrala@nitandhra.ac.in](mailto:kirankuamrgurrala@nitandhra.ac.in) with the subject marked as “JRF- C2S 2024” However, queries through phone calls or in-person will not be entertained. Also, enquiry/queries related to eligibility for the post/interpretation of rules will not be entertained.



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**Application for the Position of JRF**

Date:

**Project: “Energy Efficient Mesh Architecture Based Indigenous Neuromorphic Processor for Extreme Edge IoT Applications”.**

**Last Date of Application: 20-11-2024**

Passport size  
Recent Photograph

1. Name of the Candidate (In BLOCK LETTERS) :
2. Date of Birth (DOB) :
3. Sex (M/F) :
4. Marital Status (Single/Married) :
5. Category (General/OBC/SC/ST) :
6. Nationality :
7. Name of the Post applied for :
8. Aadhar No. :
9. Mobile No. :
10. Email Address :
11. Address :

**Correspondence:**

**Permanent:**



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12. Academic/Professional Qualifications :

(Attach the copy of the proof, mark sheet/grade card, etc)

<b>Educational Qualifications</b>	<b>Institute/ University/ Board</b>	<b>Discipline/ Field</b>	<b>Year of Passing</b>	<b>Class/ Division</b>	<b>% of Marks/ CGPA</b>
M. Tech./ M. E					
B. Tech./ B. E					
12 <sup>th</sup> Standard					
SSC/10 <sup>th</sup>					

13. GATE/NET Qualification (Yes/No) :

If Yes, Year of Qualified:

GATE Rank:

GATE Score:

14. Details of Work Experience :

(Enclose the copy of the experience certificate and/or NOC)

15. Previous Research work and Publications if any :

(enclose the first page of the publication)

**DECLARATION**

I hereby declare that all the statements made in this application are true, complete, and correct to the best of my knowledge and belief. In the event of the information being found false or incorrect or any ineligibility being detected before or after the selection, my candidature is liable to be cancelled.

Date:

Signature of the applicant

Place: