M. E. (Civil) III – Semester AICTE (Main & Backlog) Examination March-2023

TIME - TABLE

No. 128/ME/Exams/2023   TIME: 10.00 AM TO 01.00 PM   Date: 15-02-2023

DATE	STRUCTURAL ENGG.	TRANSPORTATION ENGG.	CONSTRUCTION ENGG. MANAGEMENT
10-03-2023	• Advanced Reinforced Concrete Design • Retrofitting and Rehabilitation of Structures • Design of Pre-Stressed Concrete Structures	• Railway Engineering	• Construction Planning Equipments and Methods
14-03-2023	• Waste to Energy • Advanced Concrete Technology • Structural Health Monitoring • Theory of Structural Stability	• Waste to Energy	• Industrial Safety

CONTROLLER OF EXAMINATIONS

M. E. (Electric Engg.) III – Semester AICTE (Main & Backlog) Examination March-2023

TIME - TABLE

No. 128/ME/Exams/2023   TIME: 10.00 AM TO 01.00 PM   Date: 15-02-2023

DATE	POWER ELECTRONICS /POWER ELECTRONICS SYSTEMS
10-03-2023	• Power Electronic Applications to Renewable Energy • Power Electronic Applications to Power Systems
14-03-2023	• Electric and Hybrid Electrical Vehicles

CONTROLLER OF EXAMINATIONS

M. E. (Mechanical) III – Semester AICTE (Main & Backlog) Examination March-2023

REVISED TIME - TABLE

No. 128/ME/Exams/2023   TIME: 10.00 AM TO 01.00 PM   Date: 15-02-2023

DATE	(CAD / CAM)	HEATING, VENTILATION AND AIR CONDITIONING (HVAC)
10-03-2023	• Design for Manufacture • Robotic Engineering • Mechanics of Composite Materials	• Design of Air Distribution System
14-03-2023	• Failure Analysis and Design • Additive Manufacturing Technologies & Applications	• Maintenance of HVAC Equipment

CONTROLLER OF EXAMINATIONS

M. E. (ECE) III – Semester AICTE (Main & Backlog) Examination March-2023

TIME - TABLE

No. 128/ME/Exams/2023   **TIME: 10.00 AM TO 01.00 PM**   Date: 15-02-2023

DATE	DIGITAL SYSTEMS	EMBEDDED SYSTEMS & VLSI / EMBEDDED SYSTEMS & VLSI DESIGN	EMBEDDED SYSTEMS
10-03-2023	• GNSS Signals & Receiver Technology • Speech Signal Processing	• Communication Buses & Interfaces • Field Programmable Gate Arrays	• Image and Video Processing • Privacy and Security in IoT
14-03-2023	• Advanced Computer Networks • Wireless Channel Coding Techniques	• Advanced Digital Design with Verilog HDL • SoC Design	• Real Time Operating Systems • Cloud Computing

14|2|23

CONTROLLER OF EXAMINATIONS

OSMANIA UNIVERSITY

M.Tech. (CSE) III – Semester AICTE (Main & Backlog) Examination March-2023

TIME - TABLE

No. 128/ME/Exams/2023   **TIME: 10.00 AM TO 01.00 PM**   Date: 15-02-2023

DATE	C.S.E
10-03-2023	• Mobile Computing • Software Quality & Testing • Data Science • Software Project Management
14-03-2023	• Industrial Safety • Cloud Computing • Advance Operating System • Network Security • Waste to Energy

14/2/23

CONTROLLER OF EXAMINATIONS