



Academic Wing of
Centre for Development of
Advanced Computing (C-DAC),
Thiruvananthapuram

Prospectus

- Live Projects
- Placements
- Paid Internships



Admission to Post Graduate Degree Programmes in Engineering 2021

ER&DCI Institute of Technology
Thiruvananthapuram

Post Graduate Degree Programmes in Engineering (M.Tech) are being offered to the graduates in Engineering, at ER&DCI Institute of Technology Thiruvananthapuram for awarding Masters Degree in the appropriate branches. The programmes are of 24 months duration comprising of 4 semesters.

Candidates are requested to go through this prospectus carefully. Candidates are also requested to visit the official web site of ER&DCI Institute of Technology at <http://www.erdciit.ac.in> regularly for notifications and announcements related to admission.

About ER&DCI Institute of Technology

Centre for Development of Advanced Computing (C-DAC) is the premier Research and Development (R&D) organisation of the Ministry of Electronics and Information Technology, Govt. of India for carrying out R&D in Electronics, Information Technology and associated areas. C-DAC has been at the forefront of the Information Technology revolution, constantly working in emerging technologies and

innovating and leveraging its expertise, calibre and skill-sets to develop and deploy IT products and solutions for different sectors of the economy.

ER&DCI Institute of Technology is the academic arm of C-DAC, Thiruvananthapuram. Established in 2001 with a mission to groom and nurture competitive professionals and empowering them with state-of-the-art skill-sets in Electronics and Information Technology, the Institute conducts only Post Graduate Degree Programmes in Engineering. The institute is approved by All India Council for Technical Education (AICTE) and affiliated to APJ Abdul Kalam Technological University.

ER&DCI Institute of Technology has a team of qualified faculty members who are also involved in R&D activities of CDAC. Besides, talks by experts from industry enhance the students' knowledge and practical skills. Our facilities include air conditioned class rooms, well-equipped laboratories with high-speed Internet connectivity and a well stocked technical library, projects and internships with CDAC, an excellent placement record.

2 Year Fulltime MTech Programmes Offered

Department	Stream	Prerequisite	Total Number of seats	Number of sponsored seats
Electronics & Communications	VLSI and Embedded Systems	Degree in Electronics and Communication / Electrical and Electronics / Applied Electronics / Applied Electronics and Instrumentation / Electronics and Instrumentation / Instrumentation / Electronics and Biomedical / Electronics Engineering	18* (Excluding 2 supernumerary seats for EWS category)	2 (out of 18 seats)
Computer Science	Cyber Forensics and Information Security	Degree in Computer Science and Engineering / Information Technology / Electronics and Communication / Electronics and Instrumentation / Instrumentation Engineering	18* (Excluding 2 supernumerary seats for EWS category)	2 (out of 18 seats)

*** Reservation norms decided by Govt. of Kerala shall apply and 2 supernumerary seats are reserved for Economically Weaker Section from forward category students**

**** Sponsored Seats: sponsored candidate must have at least 2 years of service on the date of application. Their employers must sponsor them and relieve them to pursue the program for the full duration of the course. GATE score is not mandatory for sponsored candidates.**

The seat distribution in both M.Tech Programmes are given below							
General	9	SC/ST	2	SEBC	5	Sponsored	2
EWS						2	



VLSI and Embedded Systems Stream

Established in 2006, the Department of Electronics, was one of the frontrunners to offer M.Tech- VLSI & Embedded System, with an objective to churn out High Calibre Hardware Engineers.

The programme mainly focuses on the ASIC/FPGA front end Design and Verification, Physical Design and Verification, Analog Mixed Signal Design and Verification, Embedded System Design and Development, Microcontrollers, Analog and Digital Circuit Design, SoC Design, Hardware Description & Verification Languages, Low Power IC Design etc. Students also get opportunity to work with the latest Electronic Design Automation tools which is in use in the industry.

The Courses handled by Scientists/Engineers working in the R&D groups of CDAC, well equipped laboratories (with Industry standard Design Tools supported with FPGA and Microcontroller development boards), opportunities to do their final year M.Tech project as live project in different R&D groups of CDAC, paid internship opportunities up to 18 months for selected students after completion of the M.Tech Programme, all sets the programme apart from that offered by other institutes.

The students are placed in various reputed firms and organizations every year by providing placement assistance through the placement cell.



Cyber Forensics and Information Security Stream

Established in the year 2012, with the objective to cater to the growing need of trained professionals in the upcoming and nascent field of cyber security, the Department of Computer Science and Engineering, ER&DCI Institute of Technology is one of the very few institutes in the country to offer industry specific programmes in Cyber Forensics and Information Security.

The course is designed to equip students with necessary knowledge and tools to fill the need for educated cyber security and cyber forensics practitioners, investigators and engineers. It also ensures that students have a basic understanding of the legal and regulatory requirements and the standards pertaining to computer security.

Courses are delivered by Scientists/Researchers /Faculty members working in the field of Cyber Forensics. The department frequently conducts seminars and invited talks with the support of experienced resource persons from the industry. Students are exposed to live projects of CDAC.

The course has been designed to develop expertise of core technical topics within the area of Cyber Security and Digital Forensics such as Information Security, Secure Programming, Network Security, Penetration Testing, Malware Analysis, File System Forensics, Windows and Linux Forensics, Image and Video Forensics ,IT law and ethics.

Students get opportunity to be placed / do Internship in CDAC. They also get campus placement in top notch companies like Deloitte, Saint Gobain, Reliance, GMR, H&R Block etc. The past two years 100% placement has been achieved.



Eligibility for Admission

Eligibilities for admission to Post Graduate Degree Programmes offered at ER&DCI Institute of Technology are given below.

- 1 The candidates shall be an Indian National.
- 2 The candidates should have studied the graduate degree programme in Engineering in an institution approved by the AICTE or in a Deemed University in India.
- 3 Candidates should have a minimum of 60% aggregate marks in the Engineering Degree examination. For SC / ST candidates a pass in the Engineering Degree course is sufficient. For SEBC (OBC) students, a minimum of 54% aggregate marks in the Engineering Degree examination is mandatory.
- 4 Candidates, who have passed AMIE / AMIETE examinations are eligible for admission, provided they have valid GATE score and a minimum of 55% marks for section B in AMIE/AMIETE examination.
- 5 Candidates who have appeared for the final semester examination can also apply, provided he / she has passed all the subjects up to and including the 6th semester exam. Confirmation of admission of such candidates shall be subject to the production of qualifying degree – before the date stipulated by the affiliated University.



Selection Procedure

Admission to the MTech programmes is based on GATE score of the candidate. In case of more than one candidate having the same GATE score, they will be ranked based on their marks (or grades) obtained in their qualifying examinations. If seats remain vacant due to lack of candidates with valid GATE score, the remaining seats are allotted to candidates who do not have valid GATE score, based on their mark (or grade) in Engineering degree examination upto sixth semester. After the last date of receiving applications, ER&DCI Institute of Technology shall publish the list of selected candidates and the list of wait listed candidates for each programme. Reservation as specified by Govt. of Kerala is applicable in the selection process.

Selected candidates shall have to report at ER&DCI Institute of Technology on the prescribed date and complete the admission process, failing which the seats will automatically be transferred to the next eligible candidates in the waiting list.

In case seats remain vacant due to lack of eligible candidates even after considering candidates without valid GATE score, one or more rounds of spot admission / walk-in admission will be conducted to fill the seats.



Submission of Application

Application form for MTech admission can be downloaded from the web site of ER&DCI Institute of Technology (<http://erdcit.ac.in>). Candidates have to take print outs of the downloaded application form and fill up all details. Copies of completed application forms should reach in both hard & soft copy by stipulated time. Soft copy of application forms, necessary documents & proof of fee payment should be scanned as a single pdf file and sent to erdcit@cdac.in before the stipulated deadline. Hard copies should be sent to ER&DCI Institute of Technology address by post so as to reach before last date. ER&DCI Institute of Technology shall not be responsible for delay in the receipt of application due to any postal or other unforeseen delay. Each application has to be accompanied by an application fee of Rs 500 by way of crossed demand draft drawn on any nationalized / scheduled bank in favour of ER&DCI Institute of Technology payable at Thiruvananthapuram. Alternatively the application fee may be transferred through NEFT. In such cases, the NEFT transaction id shall be clearly mentioned in the application form. Application fee shall not be refunded under any circumstances. Self attested copies of the documents specified in table below, should be sent along with the application.

No.	Self-attested Copy of Document / Certificate
1	Degree certificate or provisional degree certificate of qualifying examination issued by university
2	Mark sheets / grade sheets of qualifying examination issued by university
3	For general category students having CGPA between 6.0 and 6.5 and SEBC category students having CGPA between 5.4 and 5.9, percentage certificate issued by university showing the equivalent percentage of marks, in case the final grade sheet shows CGPA only and does not show equivalent marks or formula to convert CGPA into marks
4	GATE score card
5	Secondary School Leaving Certificate
6	Electoral id / Aadhaar card / passport / nativity certificate issued by revenue authority as proof of address.
7	For SEBC category students, recent caste and non-creamy layer certificate issued by Tahsildar / Village officer
8	For SC / ST category students, caste certificate issued by Tahsildar / Village officer
9	No objection certificate / relieving order from employer, if the candidate is employed
10	Certificate issued by competent authority showing percentage of disability, if the candidate is differently abled. (Ref: DoPT OM No.36035/3/2004-Esst (Res), dated 29.12.2005)
11.	For EWS from forward caste students, EWS certificate from Village Officer / Tahsildar.

Our Account details for NEFT transactions:

- **Name of Beneficiary: CDAC, Thiruvananthapuram**
- **Bank: Canara Bank**
- **Branch: CDAC Campus Branch, Vellayambalam**
- **Account no: 40192200006044 (IFSC: CNRB0014019)**

Applications should be complete in all respects. Application forms not accompanied by Demand Draft / Proof of payment through NEFT for application fee and self-attested copies of specified documents shall be considered incomplete and rejected. Applications received after the specified last date shall also be rejected.

Programme Fee

Particulars	I Semester	II Semester	III Semester	IV Semester
Tuition Fee	45000	45000	45000	45000
Spl fee	21900	21900	21900	21900
Cautious Deposit	6000			
PTA	2000			
Admission fee	1000			
University Student Administration Fee	1000*			
University Sports & Arts Fee	500*			
Exam Fee	1500*	1500*	900*	2000*
Total	78900	68400	67800	68900

***Subject to change if APJ Abdul Kalam Technological University decides to modify any fee**

First semester fee has to be paid at the time of admission. On subsequent semesters, programme fee has to be paid at the beginning of each semester.



Scholarships, Student projects, Internships and Placements

Scholarship is available for candidates with valid GATE Score as per AICTE norms for the total period of the programme.

Meritorious students of ER&DCI Institute of Technology are offered opportunity to do their student projects (third and fourth semesters) in various groups of C-DAC and involve in live projects. This will enable the students to get first-hand knowledge in state-of-the-art technologies and in industry work culture and improve their employability.

Internships are stepping stones for a student to his future professional career. It allows the student to gain valuable exposure to the workplace, provides the opportunity for skill development, and gives a competitive edge in the job search. Eligible students of ER&DCI Institute of Technology shall have the opportunity to do paid internship upto eighteen months in C-DAC Thiruvananthapuram. This facility depends upon the vacancies available and is limited to students with good academic track record.

ER&DCI Institute of Technology provides a strong base for placement and industrial training requirements of students. There is a dedicated placement cell working in the institute with the objective to provide students employment opportunity in leading organisations. Alumni from the institute are working in highly acclaimed companies including Saint-Gobain, TataElxsi, Wipro, Infosys, IBS, TCS, EY, Deloitte, GMR and government organisations including C-DAC, ADA, NIC, AAI, ISRO, Railways and as security specialist officers in nationalised and scheduled banks.

Placements statistics in year 2021 Pass outs:

Cyber Forensics and Information Security	100%
VLSI & Embedded Systems	15 out of 17 students placed

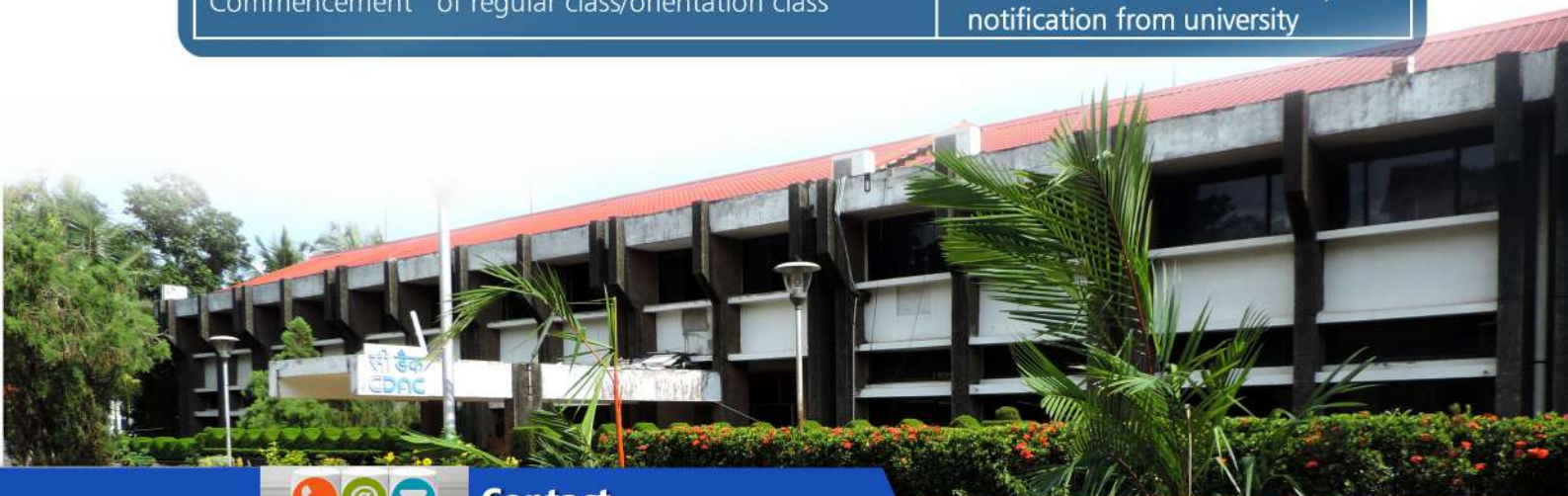
Prevention and Prohibition of Ragging

Ragging is totally banned in ER&DCI Institute of Technology premises and anyone found guilty of ragging and / or abetting ragging is liable to be punished. In case, the applicant for admission is found to have indulged in ragging in the past or if it is noticed later that he / she has indulged in ragging, admission may be refused or he / she shall be expelled from the institution.

Each of the students of the institution and his / her parent or guardian is required to submit an undertaking in this regard at the time of admission in the prescribed format which is mandatory for admission. The formats of these affidavits are available for download at our web site.

IMPORTANT DATES

Starting date of receiving application	9th August 2021
Closing date of application	31 st August 2021
Closing date for receiving hard copies of the application forms	04 th September 2021
Publication of draft rank list	08 th September 2021
Publication of final rank list	09 th September 2021
First allotment List publication	10 th September 2021
Joining / Online counseling dates after first allotment	14,15 & 16 th September 2021
Second allotment List publication	21 st September 2021
Joining / Online counseling dates after second allotment	23 & 24 th September 2021
Commencement of regular class/orientation class	Shall be announced later as per notification from university



Contact



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