PAPER-II

ELECTRONI	C SCIENCE					
Signature and Name of Invigilator						
1. (Signature)	OMR Sheet No.:					
(Name)	(To be filled by the Candidate)					
2. (Signature)	Roll No.					
(Name)	(In figures as per admission card)					
	Roll No					
D 8 8 1 4	(In words)					
Time: $1^{1}/_{4}$ hours]	[Maximum Marks : 100					
Number of Pages in this Booklet: 12	Number of Questions in this Booklet: 50					
Instructions for the Candidates	परीक्षार्थियों के लिए निर्देश					
 Write your roll number in the space provided on the top of this page. This paper consists of fifty multiple-choice type of questions. At the commencement of examination, the question booklet will be given to you. In the first 5 minutes, you are requested to open the booklet and compulsorily examine it as below: To have access to the Question Booklet, tear off the paper seal on the edge of this cover page. Do not accept a booklet without sticker-seal and do not accept an open booklet. Tally the number of pages and number of questions in the booklet with the information printed on the cover page. Faulty booklets due to pages/questions missing or duplicate or not in serial order or any other discrepancy should be got replaced immediately by a correct booklet from the invigilator within the period of 5 minutes. Afterwards, neither the Question Booklet will be replaced nor any extra time will be given. After this verification is over, the OMR Sheet Number should be entered on this Test Booklet. 	 इस पृष्ठ के ऊपर नियत स्थान पर अपना रोल नम्बर लिखिए । इस प्रश्न-पत्र में पचास बहुविकल्पीय प्रश्न हैं । परीक्षा प्रारम्भ होने पर, प्रश्न-पुस्तिका आपको दे दी जायेगी । पहले पाँच मिनट आपको प्रश्न-पुस्तिका खोलने तथा उसकी निम्निलिखित जाँच के लिए दिये जायेंगे, जिसकी जाँच आपको अवश्य करनी है : प्रश्न-पुस्तिका खोलने के लिए उसके कबर पेज पर लगी कागज की सील को फाड़ लें । खुली हुई या बिना स्टीकर-सील की पुस्तिका स्वीकार न करें । कबर पृष्ठ पर छपे निर्देशानुसार प्रश्न-पुस्तिका के पृष्ठ तथा प्रश्नों की संख्या को अच्छी तरह चैक कर लें कि ये पूरे हैं । दोषपूर्ण पुस्तिका जिनमें पृष्ठ/प्रश्न कम हों या दुबारा आ गये हों या सीरियल में न हों अर्थात् किसी भी प्रकार की त्रृटिपूर्ण पुस्तिका स्वीकार न करें तथा उसी समय उसे लीटाकर उसके स्थान पर दूसरी सही प्रश्न-पुस्तिका ले लें । इसके लिए आपको पाँच मिनट दिये जायेंगे । उसके बाद न तो आपको प्रश्न-पुस्तिका वापस ली जायेगी और न ही आपको अतिरिक्त समय दिया जायेगा । (iii) इस जाँच के बाद OMR पत्रक की क्रम संख्या इस प्रश्न-पुस्तिका पर अंकित कर दें । प्रत्येक प्रश्न के लिए चार उत्तर विकल्प (A), (B), (C) तथा (D) दिये गये हैं । आपको सही उत्तर के वृत्त को पेन से भरकर काला करना है 					
 4. Each item has four alternative responses marked (A), (B), (C) and (D). You have to darken the circle as indicated below on the correct response against each item. Example: (A) (B) (D) where (C) is the correct response. 	जैसा कि नीचे दिखाया गया है । उदाहरण : (A) (B) (□ D) जबिक (C) सही उत्तर है । 5. प्रश्नों के उत्तर केवल प्रश्न पत्र I के अन्दर दिये गये OMR पत्रक पर ही अंकित करने हैं । यदि आप OMR पत्रक पर दिये गये वृत्त के अलावा					
5. Your responses to the items are to be indicated in the OMR Sheet given inside the Paper I Booklet only. If you mark at any place other than in the circle in the OMR Sheet, it will not be evaluated.	किसी अन्य स्थान पर उत्तर चिह्नांकित करते हैं, तो उसँका मूल्यांकन नहीं होगा । 6. अन्दर दिये गये निर्देशों को ध्यानपूर्वक पढ़ें ।					
6. Read instructions given inside carefully. 7. Pough Work is to be done in the and of this booklet.	 कच्चा काम (Rough Work) इस पुस्तिका के अन्तिम पृष्ट पर करें । यदि आप OMR पत्रक पर नियत स्थान के अलावा अपना नाम, रोल 					
 Rough Work is to be done in the end of this booklet. If you write your Name, Roll Number, Phone Number or put any mark on any part of the OMR Sheet, except for the space allotted for the relevant entries, which may disclose your identity, or use abusive language or employ any other unfair means such as change of response by scratching or using white fluid, you will render yourself liable to disqualification. 	नम्बर, फोन नम्बर या कोई भी ऐसा चिह्न जिससे आपकी पहचान हो सके, अंकित करते हैं अथवा अभद्र भाषा का प्रयोग करते हैं, या कोई अन्य अनुचित साधन का प्रयोग करते हैं, जैसे कि अंकित किये गये उत्तर को मिटाना या सफेद स्याही से बदलना तो परीक्षा के लिये अयोग्य घोषित किये जा सकते हैं । 9. आपको परीक्षा समाप्त होने पर प्रश्न-पुस्तिका एवं मूल OMR पत्रक					
 You have to return the test question booklet and Original OMR Sheet to the invigilators at the end of the examination compulsorily and must not carry it with you outside the Examination Hall. You are, however, allowed to carry original question booklet and duplicate copy of OMR Sheet on conclusion of examination. Use only Blue/Black Ball point pen. 	निरीक्षक महोदय को लौटाना आवश्यक है और परीक्षा समाप्ति के बाद उसे अपने साथ परीक्षा भवन से बाहर न लेकर जायें । हालांकि आप परीक्षा समाप्ति पर मूल प्रश्न-पुस्तिका तथा OMR पत्रक की डुप्लीकेट प्रति अपने साथ ले जा सकते हैं । 10. केवल नीले/काले बाल प्वाईट पेन का ही इस्तेमाल करें । 11. किसी भी प्रकार का संगणक (कैलकुलेटर) या लाग टेबल आदि का					
11. Use of any calculator or log table etc., is prohibited.	प्रयोग वर्जित है ।					

11. Use of any calculator or log table etc., is prohibited. 12. There is no negative marks for incorrect answers. **D-88-14** P.T.O. 1

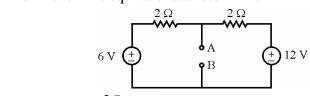
12. गलत उत्तरों के लिए कोई नकारात्मक अंक नहीं हैं।

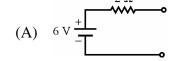
ELECTRONIC SCIENCE

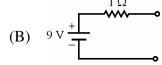
Paper – II

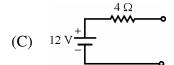
Note: This paper contains **fifty (50)** objective type questions of **two (2)** marks each. **All** questions are compulsory.

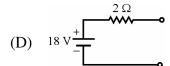
- 1. The threshold voltage of an n-channel MOSFET can be increased by
 - (A) increasing the channel dopant concentration
 - (B) reducing the channel dopant concentration
 - (C) reducing the gate oxide thickness
 - (D) reducing the channel length
- 2. The Thevenin's equivalent across AB is











- 3. The input to a differentiator is -5 V. Its output will be
 - (A) square wave

(B) 0 V

(C) +5 V

- (D) sine wave
- **4.** In successive approximation converter input to the comparator is through
 - (A) DAC

(B) Latch

(C) Flip-flop

- (D) Sample and hold circuit
- 5. The assembler directive used to give name to some value or symbol for 8086 ASM-86 is
 - (A) DD

(B) NAME

(C) EQU

(D) PROC

6. Which of the following is not an infinite loop?

```
(A) int i = 1;
while (1)
{
i ++
```

(C) int t = 0, f; while (t)

while (t) {
 f = 1;
}

(D) int y, x = 0;

do {
y = x;
}

while (x = 0);

7. In a two cavity Klystron the secondary cavity is called

(A) Buncher

(B) Velocity modulation

(C) Coupled cavity

(D) Catcher

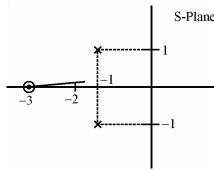
8. In FM

- (A) carrier never becomes zero
- (B) J-coefficient occasionally are negative
- (C) total power remains constant with respect to modulation index
- (D) pulse rate decreases

9. In thyristor $\frac{di}{dt}$ failure is prevented by

- (A) putting L in series with anode
- (B) putting R in series with anode
- (C) putting C in series with anode
- (D) putting RC in series with anode

10. The driving point impedance Z(s) of a network has pole – zero plot as shown, if Z(0) = 3, then Z(s) = ?



(A)
$$Z(s) = 3(s+3) / s^2 + 2s + 3$$

(B)
$$Z(s) = 2(s+3) / s^2 + 2s + 2$$

(C)
$$Z(s) = 3(s-3) / s^2 + 2s + 2$$

(D)
$$Z(s) = 2(s-2) / s^2 + 2s + 2$$

11.	The	C.E. configuration is normally prefe	erred l	pecause it provides:						
	i.	voltage gain								
	ii.	current gain								
	iii.	power gain								
	iv.	stability								
	Whi	ch is correct?								
	(A)	i, ii	(B)	i, ii, iii						
	(C)	ii, iii	(D)	ii, iii, iv						
12.	An element between the two terminals of a network to which a connection can be made is called while the branches of the tree are called									
	i.	branch								
	ii.	node								
	iii.	twig								
	iv.	loop								
	Whi	ch is correct?								
	(A)	i, ii	(B)	ii, iii						
	(C)	iii, iv	(D)	i, iv						
13.	Cons	sider the following statements regar	ding a	n RC phase shift oscillator:						
	i.	amplifier gain is positive.	Ü	•						
	ii.	amplifier gain is negative.								
	iii.									
	iv.									
		ch is correct?								
	(A)	i, ii	(B)	ii, iii						
	(C)	i, iii	(D)	i, iv						
14.	Read	I the following statements:								
	i. Gate is a combinational logic.									
	ii. JK Flip-flop in toggle mode is not combinational logic.									
	iii.	1 1 66								
	iv. Counters are sequential circuits.									
	Whi	ch is correct?								
	(A)	i, ii	(B)	i, ii, iv						
	(C)	ii, iii, iv	(D)	i, ii, iii						
15.	Whi	ch of the following peripherals prov	ide I/	O facilities ?						
	i.	8279	ii.	8155						
	iii.	8259	iv.	8255						
	(A)	i, ii	(B)	ii, iii						
	(C)	iii, iv	(D)	ii, iv						
	(-)	,	(-)	<i>)</i>						

16.	i. ii. iii. Whi	three types of loops available in C for while do-while ch loops do not operate without to i, iii	esting th	
	(C)	ii, iii	(D)	i, ii, iii
17.	i. ii. iii. Whie	N diode can not be used as Microwave switch Microwave mixer Microwave detector ch is correct ?	(D)	:: :::
		i, ii i, iii	(B) (D)	ii, iii i, ii, iii
18.	Reac	d the statements:		
	i. ii. iii.	DSB has two side bands and SS DSB has carrier and two side bands.	nds and	ne SSB has a carrier and a side band d SSB without carrier and two different side
		ch statements are correct?	(D)	** ***
	, ,	i, ii i, iii	(B) (D)	ii, iii i, ii, iii
19.	Whi	ch of the following are bidirection	nal devic	ces ?
	i.	SCR	ii.	TRIAC
	iii.	DIAC	iv.	SCS
	(A)	i, ii	(B)	ii, iii
	(C)	ii, iv	(D)	i, iv
20.	i. ii. iii.	ch of the following transducers ar thermistor thermocouple IC sensor		
	(A)	i, ii	(B)	ii, iii
	(C)	i, iii	(D)	i, ii, iii
21.		ch one of the following is not LEI		
	1.	Ga As	2.	Ga P
	3.	Si	4.	SiO ₂
	(A) (C)	1 & 2 3 & 4	(B) (D)	2 & 3 1 & 4
22.	. ,		` '	measured/determined using HALL effect?
	1.	Type of semiconductor (p or n)	2.	Band gap
	3.	Diffusion constant	4.	Carrier concentration
	(A)	1 & 2	(B)	2 & 3
	(C)	1 & 4	(D)	1 & 3

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23.	Whi	ch of	the fo	llowi	ng are	not t	the par	ts of an	AM superheterodyne receiver?	
	1.		_	asis ne	etworl	ζ.		2.	Mixer	
	3.		mplif	fier				4.	Limiter	
	(A)	1 &						(B)	1 & 3	
	(C)	1 &	4					(D)	2 & 4	
24.	The	follov	ving i	s true	for th	e mu	ltimod	le grade	d index fiber:	
	1.	The	refrac	ctive i	ndex	varies	s as a f	function	of radial distance from the centre	e.
	2.	The	refrac	ctive i	ndex	under	rgoes s	sudden (change at the cladding boundary.	
	3.	It pr	ovide	s bette	er ban	dwid	lth and	the dat	a rate than the multimode step inc	dex.
	4.	_		s the l	better	band	width		a rate than single mode step index	ζ.
	(A)	1 &						` ′	2 & 4	
	(C)	1 &	4					(D)	3 & 4	
25.	A M	OSFI	ET dif	fers fi	rom th	ne JFI	ET bec	cause		
	1.			racter				2.	the MOSFET has two gates	
	3.			has p-	n jun	ctions	S	4.	of the physical reduced size	
	(A)	1 &						(B)	1 & 3	
	(C)	1 &	4					(D)	2 & 3	
26.	Mato	h the	follo	wing	•					
	List – I							Li	st – II	
	a.	n *	p				i.	26 mV		
	b.	p-n	diode	;			ii.	$\frac{2}{ V_p }$	$I_{ m DS} \cdot I_{ m DSS}$	
	c.	JFE	Т				iii.		$(V_{GS} - V_{GST})^2$	
	d. enhancement MOSFET				ET	iv.	mass a	action law		
Codes:										
		a	b	c	d					
	(A)		i	iv	ii					
	(B)	ii	iv	iii	i					
	(C)	iv	i	ii	iii					
	(D)	i	ii	iv	iii					
27.	Mate			wing	:					
			List –						List – II	
	a.	-	arame			i.		_	e varies as the slope of i/p voltage	
	b.		erenti		~	ii.		se divis		
	c.			e recti	fier	iii.			a Q point	
	d.		gratoi	ſ		iv.	serie	es diode	e clipper	
	Cod		1.		.1					
	()	a :::	b	c	d					
	(A)	iii ::	i :::	iv	ii :					
	(B)	ii ;	iii :x/	iv ;;	i iii					
	(C)	i	iv iii	ii ;	111 ii					
D	(D)	iv	111	i	11					D 00 11
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28.	Match the foll		T 1.4 TT
	a. voltageb. constant	List – I shunt negative current	e feedback i. increase of CMRR source ii. O/P voltage attenuated by a factor 1/29
	differen	tial amplifier	·
	d. PLL	nift oscillator	iii. FSK decoderiv. decrease of O/P impedance
	Codes: a b	c d	
	(A) i iii (B) iv i	iv ii ii iii	
	(C) iii ii	i iv	
29.	(D) ii iv Match the foll		
27.		t – I	List – II
	a. I ² L	1	i. XOR
		pedance state ed inverter	ii. non-saturation logic iii. tristate
	d. ECL	ou 111 (01 (01	iv. bipolar logic
	Codes:	c d	
	(A) i iii (B) ii iv	iv ii i iii	
	(C) iii ii (D) iv iii	iv i i ii	
30.	Match the foll		
00.			• • • •
	List – I		ist – II
	a. 8086	i. 128 b	yte RAM
	a. 8086b. 8051c. 8279	i. 128 bii. 2-keyiii. 3-chip	yte RAM lockout o configuration
	a. 8086b. 8051c. 8279d. 8085	i. 128 bii. 2-keyiii. 3-chip	yte RAM lockout
	 a. 8086 b. 8051 c. 8279 d. 8085 Codes: a b 	 i. 128 b ii. 2-key iii. 3-chip iv. maxir c d 	yte RAM lockout o configuration
	 a. 8086 b. 8051 c. 8279 d. 8085 Codes: a b (A) iii i 	 i. 128 b ii. 2-key iii. 3-chiq iv. maxir c d iv ii 	yte RAM lockout o configuration
	 a. 8086 b. 8051 c. 8279 d. 8085 Codes: a b (A) iii i (B) ii i 	i. 128 bii. 2-keyiii. 3-chipiv. maxirc d	yte RAM lockout o configuration
	 a. 8086 b. 8051 c. 8279 d. 8085 Codes: a b (A) iii i (B) ii i 	 i. 128 b ii. 2-key iii. 3-chip iv. maxir c d iv ii iii iv 	yte RAM lockout o configuration
31.	a. 8086 b. 8051 c. 8279 d. 8085 Codes: a b (A) iii i (B) ii i (C) iv i (D) i iii Match the foll	 i. 128 b ii. 2-key iii. 3-chip iv. maxir c d iv ii iii iv ii iii iv ii owing: 	yte RAM lockout configuration mum mode
31.	a. 8086 b. 8051 c. 8279 d. 8085 Codes: a b (A) iiii i (B) ii i (C) iv i (D) i iii Match the foll	 i. 128 b ii. 2-key iii. 3-chip iv. maxir c d iv ii iii iv ii iii iv ii owing: - I 	yte RAM lockout c configuration mum mode List – II
31.	a. 8086 b. 8051 c. 8279 d. 8085 Codes: a b (A) iii i (B) ii i (C) iv i (D) i iii Match the foll	 i. 128 b ii. 2-key iii. 3-chip iv. maxir c d iv ii iii iv ii iii iv ii iv ii 	yte RAM lockout c configuration mum mode List – II i. memory storage ii. if-then-else
31.	a. 8086 b. 8051 c. 8279 d. 8085 Codes: a b (A) iii i (B) ii i (C) iv i (D) i iii Match the foll List a. associati b. # define c. auto	i. 128 b ii. 2-key iii. 3-chip iv. maxir c d iv ii iii iv iii iii iv ii owing: - I ivity	List – II i. memory storage ii. if-then-else iii. operators with equal precedence
31.	a. 8086 b. 8051 c. 8279 d. 8085 Codes: a b (A) iii i (B) ii i (C) iv i (D) i iii Match the foll List a. associati b. # define c. auto	 i. 128 b ii. 2-key iii. 3-chip iv. maxir c d iv ii iii iv ii iii iv ii iv ii 	yte RAM lockout c configuration mum mode List – II i. memory storage ii. if-then-else
31.	a. 8086 b. 8051 c. 8279 d. 8085 Codes: a b (A) iii i (B) ii i (C) iv i (D) i iii Match the foll List a. associate b. # define c. auto d. conditio Codes: a b	 i. 128 b ii. 2-key iii. 3-chip iv. maxir c d iv ii iii iv ii iii iv ii owing: I ivity 	List – II i. memory storage ii. if-then-else iii. operators with equal precedence
31.	a. 8086 b. 8051 c. 8279 d. 8085 Codes: a b (A) iiii i (B) ii i (C) iv i (D) i iii Match the foll List a. associat b. # define c. auto d. conditio Codes: a b (A) i iii	i. 128 b ii. 2-key iii. 3-chip iv. maxir c d iv ii iii iv iii iv ii owing: -I ivity c d iv ii	List – II i. memory storage ii. if-then-else iii. operators with equal precedence
31.	a. 8086 b. 8051 c. 8279 d. 8085 Codes: a b (A) iii i (B) ii i (C) iv i (D) i iii Match the foll List a. associate b. # define c. auto d. conditio Codes: a b	 i. 128 b ii. 2-key iii. 3-chip iv. maxir c d iv ii iii iv ii iii iv ii owing: I ivity 	List – II i. memory storage ii. if-then-else iii. operators with equal precedence

32. Match the following:

List - I

i. envelope detection

List - II

- a. DSB-SC modulation
- b. SSB-modulation
- ii. Foster Seeley
- AM-demodulation c.
- iii. Weaver's method
- d. Phase-shift detection

b

iii

i

ii

iv. Balanced modulator

Codes:

- a
- d c
- (A) i iv
- ii iii
- (B) iv
- i ii
- (C) ii
- iv iii
- (D) iii
- i iv
- 33. Match the following:

List – I

List - II

- Snubber circuit a.
- **SCR** i.
- b. Inverter
- ii. High efficiency
- Phase control c.
- $\frac{dv}{dt}$ protection iii.
- d. **SMPS**
- iv. UJT

Codes:

- b c d a
- (A) iii i iv ii
- iii (B) i ii iv
- (C) iv ii i iii
- i iii (D) ii iv
- 34. Match the following:

List - I

List - II

- Miller sweep in CRO a.
- i. storage oscilloscope
- Study of transients b.
- ii. integrator
- Kokrotkoff sound c.
- phase measurement iii.
- d. Lissajous pattern
- blood pressure iv.

Codes:

- b d a c
- (A) i ii iii iv
- (B) iii ii i iv
- (C) ii i iviii
- (D) iv ii i iii

35. Match the following:

List – I List – II

- a. Power efficient transmission i. SSB-SC
- b. Most bandwidth efficient transmission of ii. VSB voice signal
- c. Simplest receiver iii. FM
- d. Bandwidth efficient transmission of iv. AM signals with significant d.c. component

Codes:

	a	b	c	d
(A)	i	ii	iii	iv
(B)	iii	i	iv	ii
(C)	iv	ii	iii	i
(D)	ii	iv	i	iii

Directions: O. Nos. 36 to 45:

The following items consist of two statements, one labelled as "Assertion (A)" and the other labelled as the "Reason (R)". You are to examine the two statements carefully and decide if the Assertion (A) and the Reason (R) are individually true and if so whether the reason is a correct explanation of the assertion. Select your answer to these items using the codes given below and mark your answer accordingly.

Codes:

- (A) Both (A) and (R) are true and (R) is the correct explanation of (A).
- (B) Both (A) and (R) are true, but (R) is not the correct explanation of (A).
- (C) (A) is true, but (R) is false.
- (D) (A) is false, but (R) is true.
- **36.** Assertion (A): MOS ICs based on MOSFET structure find wide applications in digital field.
 - **Reason (R)** : MOS ICs have small size and are easy to fabricate.
- **37. Assertion** (A): The series pass transistor in a regulator is in class A mode.
 - **Reason (R)** : Class A is a switching mode and yields high efficiency.
- **38.** Assertion (A): The most commonly used amplifier in S/H circuit is unity gain NINV amplifier.
 - **Reason** (**R**) : At the sampling state signal building is not desired.
- **39.** Assertion (A): In some applications it is required to delay pulse train by some number of clock periods.
 - **Reason (R)**: For delay operation serial in parallel out shift registers are useful.

40. Assertion (A): Interfacing is a technique to make operation of peripheral or I/O device compatible with that of a micro-processor.

Reason (R) : Some peripherals and I/O devices are not TTL compatible.

41. Assertion (A): 'C' uses many data types such as integers (short and long) and float etc.

Reason (R): Conversion specifier used for short unsigned integer is % lu.

42. Assertion (A): The two types of optical sources used in transmitter of optical communication link are LED and LASER.

Reason (R) : LASERs are costly hence not preferred for small distance low cost systems.

43. Assertion (**A**): In applications such as FM and FSK, VCO plays an important role. **Reason** (**R**): The frequency control is easily possible by varying d.c. voltage.

44. Assertion (A): SCR and SCS belong to the thyristor category.Reason (R): What distinguishes SCS from SCR is that SCS is two gate device.

45. Assertion (A): Oscilloscope provides graphical representation of time varying signals. **Reason** (R): Bandwidth is the limitation of oscilloscope.

Read the passage and answer the questions 46 to 50 that follow on your understanding of passage:

Optical fibre communication is one among the new techniques have come up and are extensively used for communication purposes. The portion of the e.m. spectrum encompasses the optical region falls in the wavelength of 50 nm (Ultraviolet) to approximately 100 μm (Infrared) which includes visible light spectrum from 400 nm (Violet) to 700 nm (Red). The order of frequency corresponding to these wavelengths falls in the range 10^{14} to 10^{18} Hz. There are two types of transmission media used for optical communication i.e., atmospheric channel and guided wave channel. Out of the two, guided wave channel provide much more reliable and versatile medium of communication. The medium used for guided channels in optical range are non-metallic guides popularly known as optical fibre. The optical fibre link used for communication applications has several advantages over conventional cupper cable links which makes possible to send much more data over long distances with negligible attenuation. Although some difficulties do exist in the making of optical fibre set and there are some challenges being faced yet this type of communication mechanism has made a great impact in the present scenario.

46. A multimode step-index fibre has glass core $(n_1 = 1.5)$ and fused quartz cladding $(n_2 = 1.46)$, which one of the following is the value of acceptance angle?

(A) 20.2°

(B) 21.2°

(C) 22.2°

(D) 76.7°

- **47.** Following is not the usual classification of an optical fibre :
 - (A) single mode step index
 - (B) single mode graded index
 - (C) multimode step index
 - (D) multimode graded index
- **48.** When atoms in Direct bandgap semiconductors move from higher energy state (E_2) to lower energy state (E_1) and emission of light takes place, the energy of emitted photon is given as
 - (A) $h v_{12} = E_2 E_1$
 - (B) $h v_{12} = \frac{E_2}{E_1}$
 - (C) $h v_{12} > E_2 E_1$
 - (D) $h v_{12} < E_2 E_1$
- **49.** Which of the following are the cases of signal attenuation?
 - 1. Splicing
 - 2. Intermodal Delay
 - 3. Scattering
 - 4. Chromatic Dispersion
 - (A) 1 & 3
 - (B) 2 & 3
 - (C) 1 & 4
 - (D) 2 & 4
- **50.** The following are correct about a semiconductor LASER:
 - 1. It requires population inversion
 - 2. It has shorter lifetime than LED
 - 3. It demonstrates spontaneous emission phenomenon
 - 4. It generates monochromatic incoherent light.

Find out the correct answer:

- (A) 1 & 2
- (B) 1 & 3
- (C) 1 & 4
- (D) 2 & 4

UGC NET DECCEMBER - 2014 PAPER-2 Date. 12/06/2015 Pg. 76 SUBJECT CODE & NAME: - (88) Electronic Science | QNO ANS | QNO ANS | QNO ANS | | 76 1 A | 26 C | 51 _____ | 2 B | 27 A | 52 | 77 | 3 B | 28 B | 53 | 78 _____ | 4 A | 29 D | 54 | 79 _____ | 80 5 C | 30 C | 55 | 6 C | 31 B | 56 | 81 7 D | 32 B | 57 _____ | 8 A | 33 A | 58 | 83 | 9 A | 34 C | 59 | 84 ______ | 10 B | 35 B | 60 | 85 | 86 | 11 B | 36 A | 61 _____ | 12 Z | 37 C | 62 | 87 | 13 B | 38 A | 63 | 14 B | 39 B | 64 | 89 | 15 D | 40 A | 65 | 90 | 91 | 16 B | 41 C | 66 _____ | 17 B | 42 B | 67 | 92 _____ | 18 A | 43 A | 68 | 93 19 B | 44 A | 69 ______ | 20 A | 45 C | 70 | 95 _____ | 21 C | 46 A | 71 | 96 -----| 22 B | 47 B | 72 | 23 C | 48 A | 73 | 24 Z | 49 D | 74 | 99 | _____ | 25 Z | 50 A | 75 | 100

Z=ALL OPTIONS ARE CORRECT/ 1=A,B/ 2=A & C OPTIONS ARE CORRECT/ 3=A & D OPTIONS ARE CORRECT/ 4=B & C OPTIONS ARE CORRECT/ 5=B & D OPTIONS ARE CORRECT/ 6=D & C OPTIONS ARE CORRECT/ 7=A, C & D OPTIONS ARE CORRECT/ 8=A, B & C OPTIONS ARE CORRECT